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24309 1590 06/02/2006

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Pat Tompkins	(Depositor's name)
Pat Tompkins	(Signature)
Keith A. Chanroo	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/981,503	10/16/2001	L. James Hwang	X-953 US	6920

TITLE OF INVENTION: METHOD AND SYSTEM FOR MODELING AND AUTOMATICALLY GENERATING AN ELECTRONIC DESIGN FROM A SYSTEM LEVEL ENVIRONMENT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	09/05/2006
EXAMINER	ART UNIT	CLASS-SUBCLASS		09/05/2006 CNGUYEN1 00000031 240040 09981503	
PHAN, THAI Q	2128	703-020000	71 FC:1501	1400.00 DA	

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1 LeRoy D. Maunu

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

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XILINX, INC.

San Jose, California 95124

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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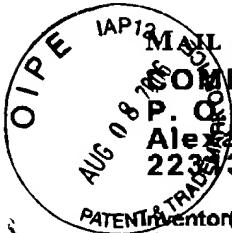
Date August 8, 2006

Typed or printed name

Registration No. 36,480

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P. O. Box 1450
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Inventor(s): L. James Hwang et al.

Assignee: XILINX, INC.

Serial No.: 09/981,503

conf.no. 6920

Filed: October 16, 2001

Title: A Method and System for Modeling and Automatically Generating an Electronic Design
from a System-Level Environment

Docket No.: X-953 US

Enclosed: Return Receipt Postcard
 Fee Transmittal - Part B

Date: August 8, 2006

VIA FACSIMILE 571-273-2885

Atty/Sec: KAC/pat